

Notice of References Cited	Application/Control No. 10/693,288	Applicant(s)/Patent Under Reexamination LEE, HEON	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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	C	US-2004/0063223	04-2004	Costrini et al.	438/003
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NON-PATENT DOCUMENTS

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	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 671-72.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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